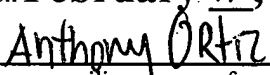



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Seitz, et al.  
Serial No. : To Be Assigned  
Filed : February 11, 2004  
For : DRAM WITH VERY SHALLOW TRENCH ISOLATION  
Examiner : To be assigned  
Group Art Unit : To be assigned

<p><b><u>Express Mail Label: EL 914767327US</u></b> I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 in an envelope addressed to: MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 on <b>February 11, 2004.</b>  _____ Name of person mailing paper or fee  _____ Signature of person mailing paper or fee</p>
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MS Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT  
WITHIN THREE MONTHS OF FILING OR  
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. §1.97(b))**

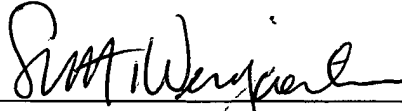
Sir:

Applicant submits this communication in compliance with 37 C.F.R. §§1.56, 1.97 and 1.98. Attached to this communication are Form PTO/SB/08A and PTO/SB/08B, identifying the publications for which consideration is requested. Copies of the references are enclosed.

This Information Disclosure Statement is being filed concurrently with a new patent application and therefore no fee should be required. However, should it be determined that any fees are due in connection with this communication, authorization is hereby given to charge Deposit Account No. 23-1703.

Dated: February 11, 2004

Respectfully submitted,



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Scott T. Weingaertner

Reg. No. 37,756

Agent's Direct Number: (212) 819-8404

Customer Number 007470

(212) 819-8200

Enclosures

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	To Be Assigned
				Filing Date	February 11, 2004
				First Named Inventor	Mihel Seitz
				Art Unit	To Be Assigned
				Examiner Name	To Be Assigned
				Attorney Docket Number	1115747-0002//2002P50545US
Sheet	1	of	2		

[illegible][illegible]

Examiner Signature		Date Considered		
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	To Be Assigned
				Filing Date	February 11, 2004
				First Named Inventor	Mihel Seitz
				Art Unit	To Be Assigned
				Examiner Name	To Be Assigned
Sheet	2	of	2	Attorney Docket Number	1115747-0002//2002P50545US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		U. GRUENING, et al, "A Novel Trench DRAM Cell with a VERTical Access Transistor and BuriEd STRap (VERI BEST) for 4Gb/16Gb," 0-7803-5413-3/99, 1999 IEEE, pp. 1-4.	
		W. NEUMUELLER, et al., "DRAM Technology For Today's Market And Future DRAM Generations," pp. 1-8.	
		C.J. RADENS, "An Orthogonal 6F <sup>2</sup> Trench-Sidewall Vertical Device Cell for 4Gb/16Gb DRAM," 0-7803-6441-4/00 2000 IEEE, pp. 1-4.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

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